U.S.Department of Commerce Form PTO-1449 Serial No. 09/502,675 Atty Docket 0756-2101 Patent and Trademark Office (Rev. 8-83) Applicants: Shunpei YAMAZAKI et al. **CLOSURE STATEMENT** Group Art Unit: 2812 Filing Date: February 11, 2000 OTHER DOCUMENT (Including Author, Title, Date, Pertinent Pages, Etc.) Examiner Initial Inventors: Shunpei YAMAZAKI et al., "Semiconductor Device and Fabrication Method Thereof' Filing Date: July 5, 2000, Specifications and Drawings for Application Serial No. 09/610,217 Inventors: Shunpei YAMAZAKI et al., "Semiconductor Device and Manufacturing Method Thereof' Filing Date: July 6, 2000, Specifications and Drawings for Application Serial No. 09/610,753 Inventor: Shunpei YAMAZAKI, "Semiconductor Device and Method of Fabricating the Same" Filing Date: December 23, 1999, Specifications and Drawings for Application Serial No. 09/471,359 Inventor: Shunpei YAMAZAKI, "Semiconductor Device and Fabrication Method Thereof" Filing Date: February 22, 2000, Specifications and Drawings for Application Serial No. 09/510,734 Inventor: Shunpei YAMAZAKI, "Method for Manufacturing an Electrooptical Device" Filing Date: March 22, 2000, Specifications and Drawings for Application Serial No. 09/533,175 Inventor: Shunpei YAMAZAKI, "Semiconductor Device and Method of Manufacturing the Same" Filing Date: February 7, 2000, Specifications and Drawings for Application Serial No. 09/498,646

RECEIVED

APR 17 2M2

TC 2800 FRAIL ROOM

Examiner

Date Considered

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this formwith next communication to applicant.